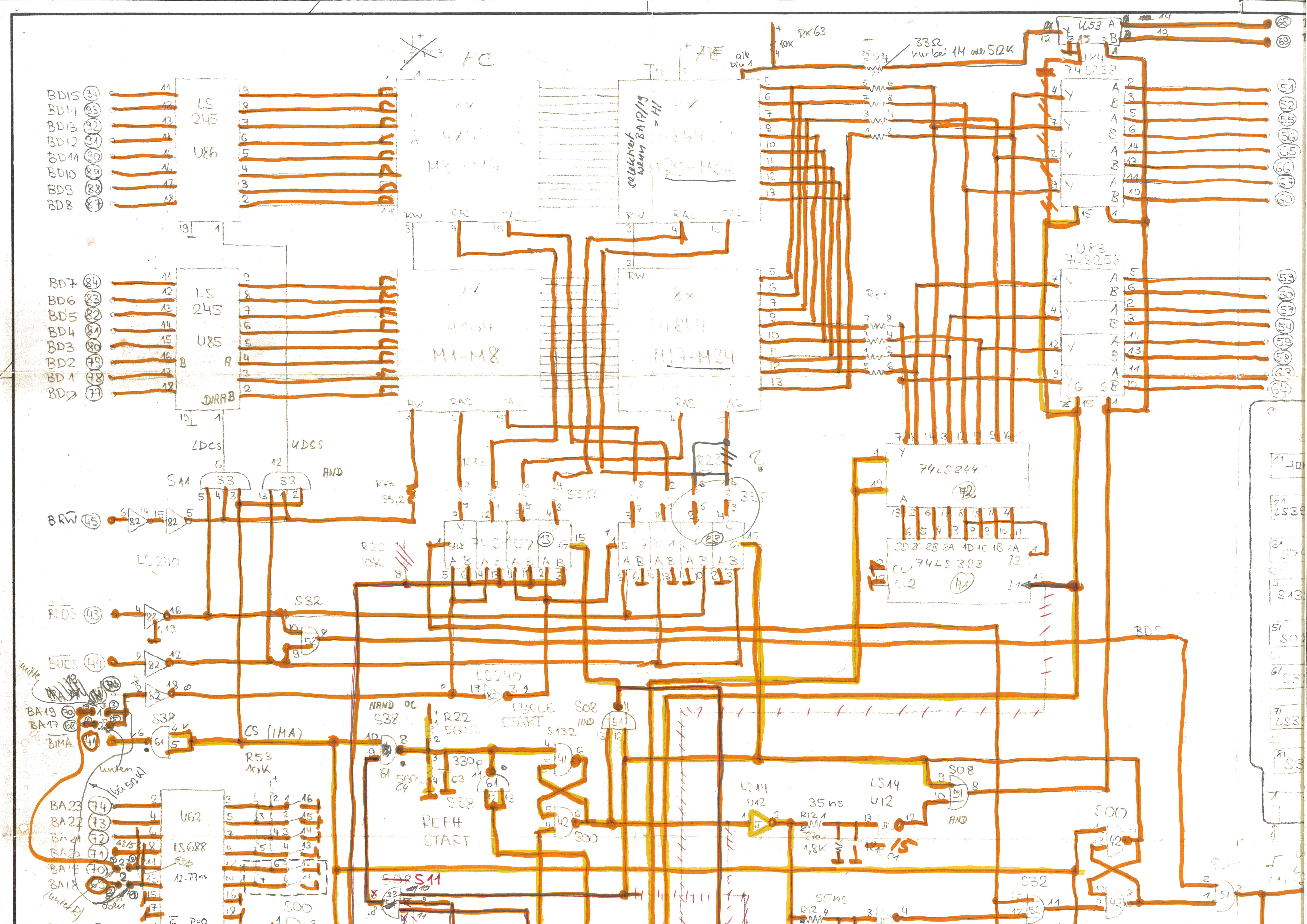
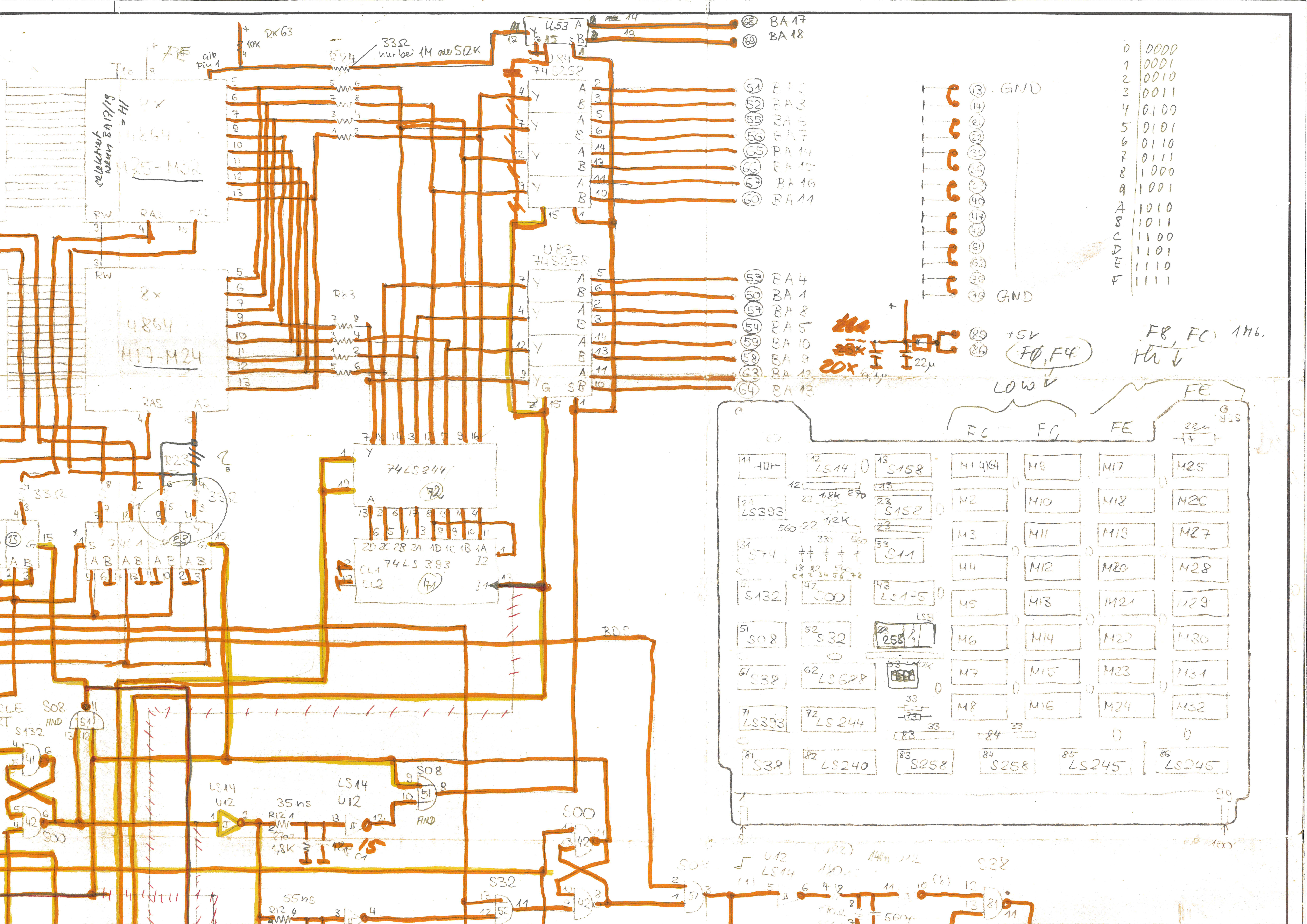
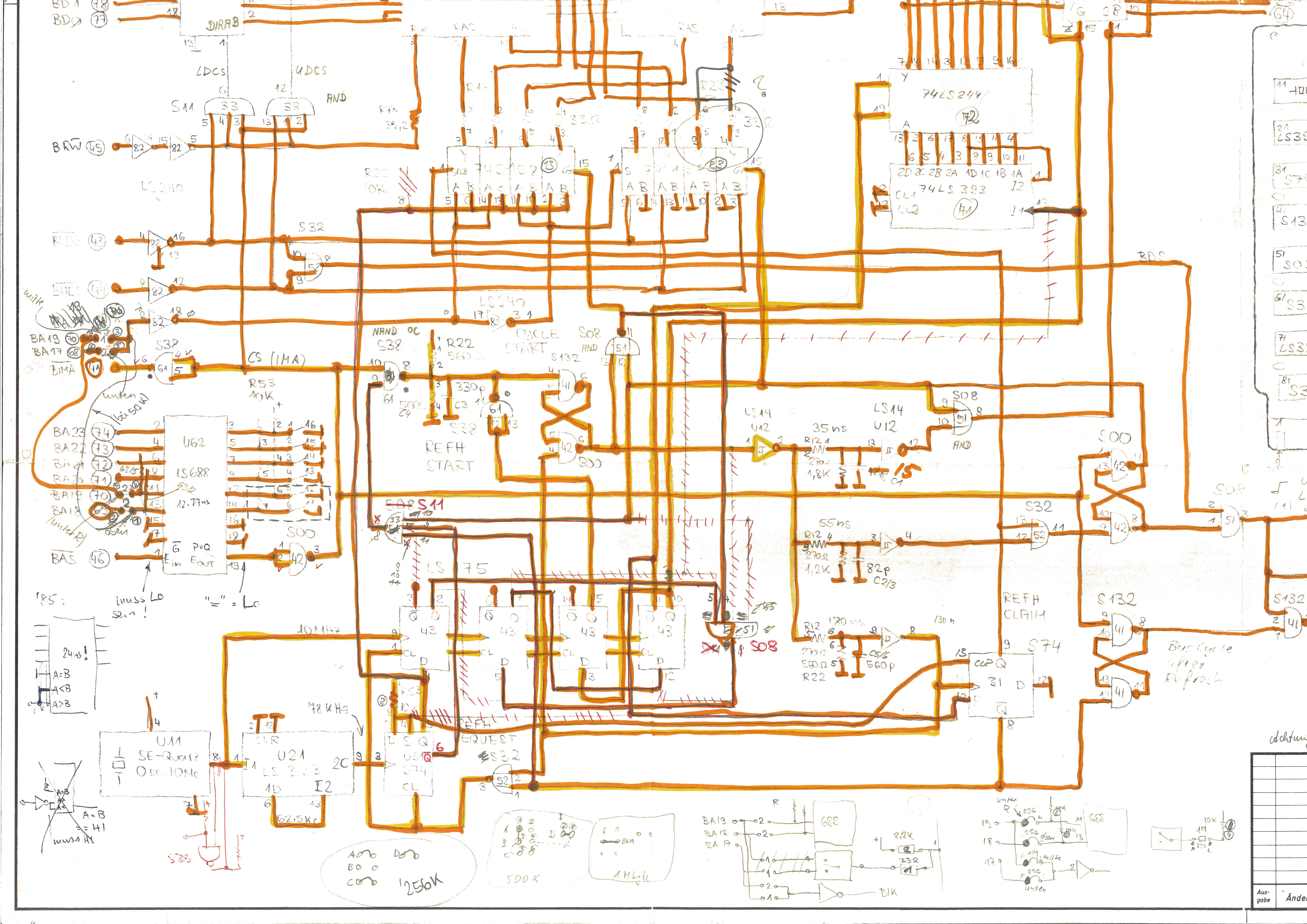


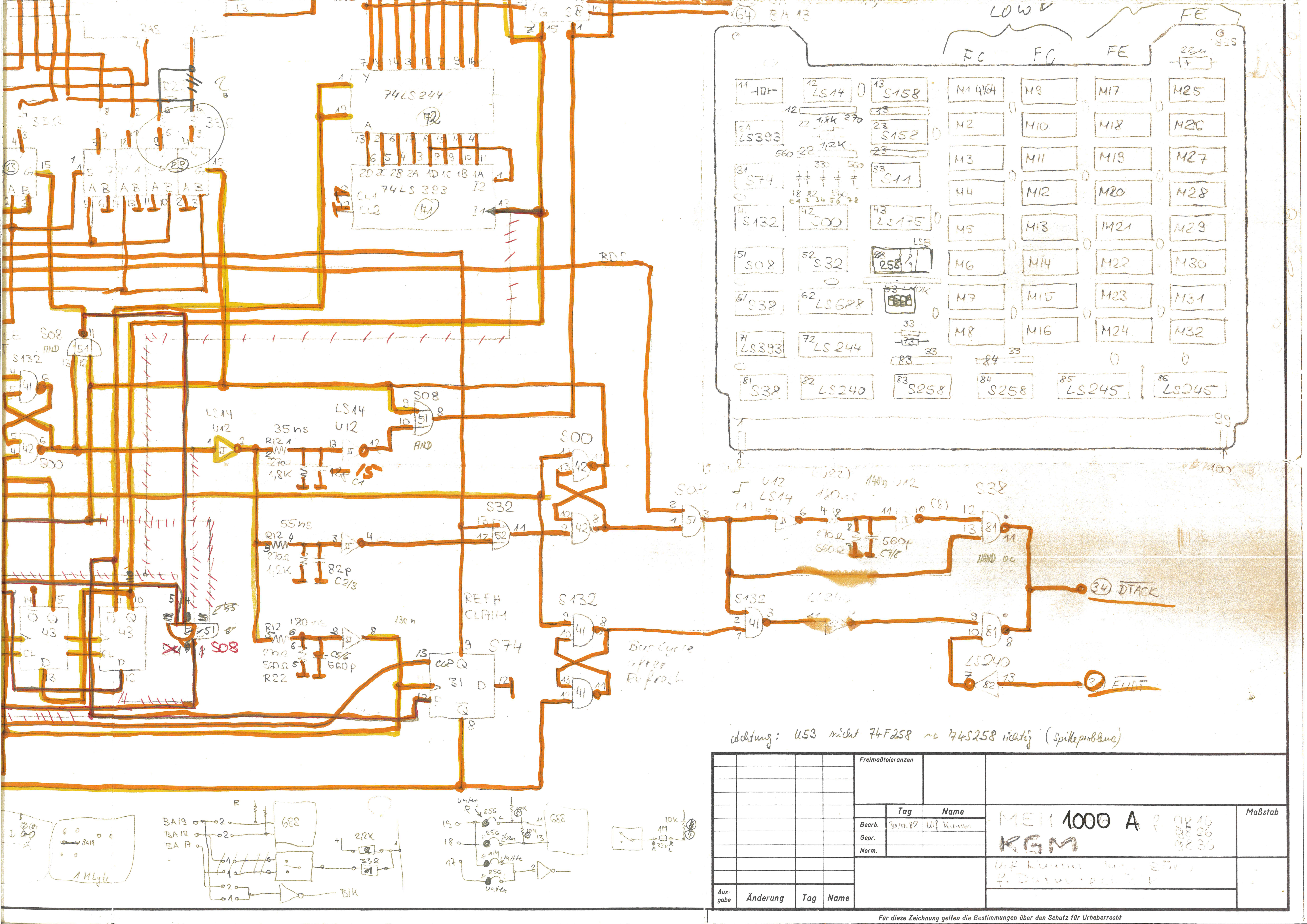
LS 245
U86
LS 245
U85
DIR A8
LDCS
UDCS
AND
BRW
BLDS
BUDS
BIMA
CS (IMA)
R53
10K
U62
LS 688
REFH
START
CYCLE
START
74LS 244
74LS 393
74S257
74S258
U83
U53
33Ω
nur bei 1M am S12K
10K
alle Pin 1
selektiert wenn BA17/19 = Hi
M1-M8
M17-M24
M25-M32
FC
FE
35 ns
55 ns
LS14
U12
BDS
BA19
BA17
BA23
BA22
BA21
BA20
BA19
BA18
BD15
BD14
BD13
BD12
BD11
BD10
BD9
BD8
BD7
BD6
BD5
BD4
BD3
BD2
BD1
BD0
R22
560Ω
330p
NAND OC
mitte
unten
oben



U53
U84
74S258
U83
74S258
74LS244
74LS393
BA17
BA18
BA4
BA1
BA8
BA5
BA10
GND
+5V
F0, F4
F8, FC
LOW
FE
BDS
S08
S132
S00
LS14
U12
35ns
55ns
S32
S38



BD1
BDØ
DIRAB
LDCS
UDCS
AND
S11
BRW
LS240
BDS
S32
LS240
CYCLE START
S08 AND
NAND OC
S38
R22 560Ω
330p
REFH START
S132
S00
LS14 U12
35 ns
LS14 U12
AND
S08
74LS244
74LS393
74LS138
BA19
BA17
BIMA
CS (1MA)
R53 10K
BA23
BA22
BA21
BA20
BA19
BA18
U62
LS688
12-77 ns
BAS
S00
G P=Q
E in E OUT
muss Lo sein!
"=" = Lo
LS175
S11
55 ns
82p
REFH CLAIM
130 ns
560p
S74
Bus Cycle after Refresh
S132
10 MHz
78 KHz
U11 SE-Quarz Osc. 10Mc
U21 LS 393
REFH REQUEST
S32
625 kc
A=B
A<B
A>B
A=B = H1
muss H1
256K
500K
1 Mbyte
BA19
BA18
BA17
688
2,2K
S32
BIK
unten
mitte
oben
achtung
Aus-gabe
Änder



				Freimaßtoleranzen			Maßstab
					Tag	Name	
				Bearb.	30.10.82	Ulf Kumm	MEM 1000 A
				Gepr.			KGM
				Norm.			
Ausgabe	Änderung	Tag	Name				